

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 08-335040

(43)Date of publication of application : 17.12.1996

(51)Int.Cl. G09C 1/00
H04H 1/00
H04H 1/02
H04L 9/00
H04L 9/10
H04L 9/12
H04N 7/167

(21)Application number : 07-159771

(71)Applicant : FUJITSU LTD

(22)Date of filing : 02.06.1995

(72)Inventor : AKIYAMA RYOTA

MUNAKATA AKIO

KOGA YUZURU

ISHIZAKI MASAYUKI

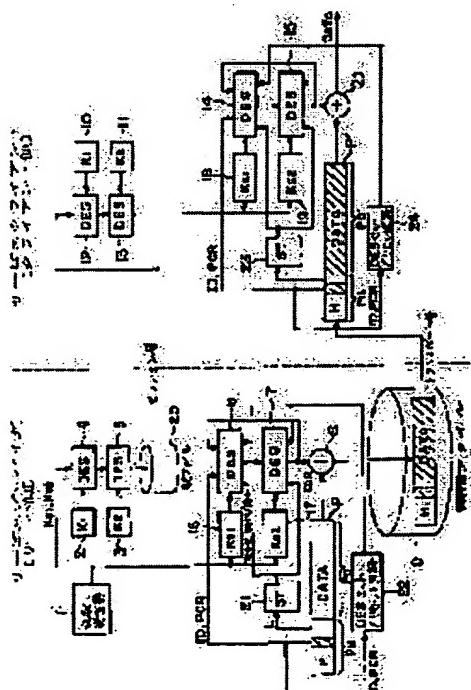
YOSHIOKA MAKOTO

(54) ENCIPHERING PROCESSING SYSTEM

(57)Abstract:

PURPOSE: To provide an enciphering processing system in which the frequent change of key is reduced by reinforcing the enciphering algorithm, and the information transferring efficiency between a service provider and a service client can be improved.

CONSTITUTION: A random number generator 1 generates a first title key Ks1 and a second title key Ks2 on the basis of random numbers. A first DES enciphering circuit for title 6 enciphers the input information with the first title key Ks1. The initial value of the input information corresponds to the data identification ID and relative time information (PCR) extracted from the header of a packet. After the encipherment of the initial value was completed, the enciphered result of a second DES



enciphering circuit for title 7 becomes the input information. The second DES enciphering circuit for title 7 enciphers the enciphered result value by the first DES enciphering circuit 6 with the second title key Ks2. An exclusive OR circuit 8 outputs the exclusive OR between the data stored in the packet and the enciphered result value of the second DES enciphering circuit 7. This forms the enciphering data.